

LAB MANUAL

Digital Logic Design Lab

B.Tech. III Semester CSE

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Electronic Circuits Logic Design Lab Manual

Paul B. Zbar



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FPGA Implementation of Digital Design Bekkay Hajji,Adel Mellit,Loubna Bouselham,2022-03-21 This book introduces the FPGA technology used in the laboratory sessions and provides a step by step guide for designing and simulation of digital circuits It utilizes the VHDL language which is one of the most common language used to describe the design of digital systems The Quartus II Xilinx ISE 14 7 and ModelSim software are used to process the VHDL code and make simulations and then the Altera and Xilinx FPGA platforms are employed to implement the simulated digital designs The book is composed of four parts The first part of this book has two chapters and covers various aspects FPGA architectures ASIC vs FPGA comparison FPGA design flow and basic VHDL concepts necessary to describe the design of digital systems The second part of the book includes three chapters that deal with the design of digital circuits such as combinational logic circuits sequential logic circuits and finite state machines The third part of the book is reserved for laboratory projects carried out on the FPGA platform It is a largely hands on lab class for design digital circuits and implementing their designs on the Altera FPGA platform Finally the fourth part of this work is devoted to recent applications carried out on FPGAs in particular advanced techniques in renewable energy systems The book is primarily intended for students scholars and industrial practitioners interested in the design of modern digital systems Subject Guide to Books in Print ,1993 **Digital Logic Simulation**

with CPLD Programming Steve Waterman,2003 This lab manual written around software and hardware developments of the past ten years focuses on the fundamentals of digital electronics and use of Max Plus II software by Altera Corporation Lab sequences start with digital gates and logic control circuits progress to MSI devices latches and flip flops and cover clock dependent circuits and the LPM_MACRO functions available in the software For individuals preparing to be computer technicians Advanced Parallel Processing Technologies Chao Li,Xuehai Qian,Dimitris Gizopoulos,Boris Grot,2025-11-03

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